

WHAT THE INVENTION CLAIMED IS

1. An interleaving management method for upgrading a data processing speed of a flash memory, comprising a plurality of flash memory cells, wherein each of said flash memory cells comprises a plurality of blocks for reading and writing data and a plurality
5 of pages in each said blocks, the method comprising:

continuously writing data into said plurality of flash memory cells, wherein when writing two or more sets of sectors into said plurality of flash memory cells, write a first sector into a first flash memory cell, and while the process of writing said first sector into said first flash memory cell is ongoing, a second flash memory cell is enabled so that a
10 second sector can be written into said second flash memory cell.

2. The interleaving management method according to claim 1, wherein said a plurality of flash memory cells for writing data continuously are arranged in an interleave structure.

3. The interleaving management method according to claim 1, wherein said
15 plurality of flash memory cells is used in a mother and child structure, said mother and child structure possessing two physical features constitute one logical address, so that for writing data, transferring and erasing steps in said flash memory cells can be avoided, thus a life time of said flash memory cells can be extended and also data processing speed of said flash memory cells can be increased.

20 4. The interleaving management method according to claim 2, wherein said flash memory cells are arranged in a group constituting the interleaving structure, correspondingly using copy back command, wherein a 64 mb flash memory cell is divided into four zones, each zone has 1024 blocks and each block has 32 pages, and four

64 mb flash memory cells constituting said interleaving structure comprises 4 zones, wherein each zone has 1024 blocks and each block has 128 pages.

5. The interleaving management method according to claim 1, wherein said interleaving management method for managing data processing of a plurality of flash memory cells is suitably applied in a hosting device, wherein said hosting device comprises, a portable ROM, a card reader in USB1.1 series, or a portable ROM, card reader in USB2.0 series, or an IDE/PCMCIA interface.